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SUMMARY

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- Generalist** with interests in **programming languages** / **dev tools** / **data intensive computing**

EDUCATION

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**Masters in Computer Science – Purdue**, West Lafayette, IN \_\_\_\_\_ Spring 2016–Fall 2018

- Workshop paper*: Mergeable Types, ICFP ML Workshop, 2017 – Contributed by implementing an OCaml AST transformer to auto derive 3-way merge functions for recursive data types.
- Courses*: Distributed systems, Advanced database systems (Relational and non-relational), Programming Language Theory, Algorithm design and analysis

**Bachelors in Computer Engineering – Purdue**, West Lafayette, IN \_\_\_\_\_ Fall 2011–Spring 2015

- Courses*: Circuit analysis, Computer design and prototyping, Signals and systems, Data structures and algorithms, Discrete Mathematics, Advanced C, Intro to AI, Intro to Infosec, Compilers, Operating Systems

MY ...

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- Writeups** – <https://sransara.com/notes/>
- Open-source contributions** – <https://github.com/search?q=author:sransara+type:pr>

SOME PROJECTS

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- Python generic TypedDict** *typed python* – <https://github.com/sransara/py-generic-typeddict>
- Multiplayer chess variation** *typescript* – <https://utopia.buddychess.com/>
- SOCK5 Proxy** *go* – <https://github.com/sransara/odd-socks>
- Mergeable functional datastructures** *ocaml* – <https://github.com/sransara/mtypes-lib>

WORK

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**Software Engineer – Epistemic AI**, Remote \_\_\_\_\_ April 2021–Present

- Implemented the project to introduce logging and observability across the platform
- Led transition to Bazel based build system and developer tooling for multi-language monorepo
- Profiled and optimized hot paths in Python code base to reduce latency
- Build and document deployment + data pipeline infrastructure and tooling
- Developed specification, frontend & backend for parallelized lazy loading to improve user experience
- Experience with*: Python, Rust, Typescript, React, Bazel, Graph databases, Terraform, AWS, K8S

**Software Engineer: Hardware test tools – Cisco**, San Jose, CA \_\_\_\_\_ June 2019–April 2021

- Building infrastructure for automating validation of circuit diagrams and PCB layouts to their specifications
- Mentor new team members on coding standards, best practices and version control
- To aid a refactoring project, develop a tool to convert some non-trivial Perl syntactic constructs to Python
- Experience with*: Mypy, Python, Perl, SKILL (A Lisp for EDA tools), EDA tools, ETL

**Test Engineer: Hardware test tools – Jabil Circuit**, San Jose, CA \_\_\_\_\_ Jan 2019–June 2019

- Extending the ETL pipeline, network tools, yield analysis tools for an HPC hardware emulation platform
- Experience with*: Go, Python, CI/CD, Design for testing in electronics mass manufacturing

**Test Engineering Intern: Hardware test tools – Jabil Circuit**, San Jose, CA \_\_\_\_\_ Summer 2018

- Building an ETL pipeline for live analysis of testcell grid data (*Jabil - Design Best Practices* competition)
- Experience with*: Data mining-visualization-analysis, Database design, Go, HTML/CSS/JS & React

**Graduate Research Assistant – Computer Science, Purdue** \_\_\_\_\_ Jan 2017–Dec 2018

- Explore mergeable functional datastructures and OCaml AST transformers (PPX)
- Experience with*: Database consistency and concurrency control, OCaml, Git internals

**Undergraduate Teaching Assistant – Computer Engineering, Purdue** \_\_\_\_\_ Jan 2015–April 2015

- Guide students in taking design decisions to develop a multicore pipelined MIPS processor

**Student Software Developer – Krannert Computing, Purdue** \_\_\_\_\_ May 2012–Dec 2016

- Develop web and desktop apps. *Experience with*: C#, .NET, Database design, HTML/CSS/JS

OTHER

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- Recognized in Google security hall of fame for disclosure of XSS bugs in Gmail: 2012 July, 2012 October